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Sheet 1

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

of 3

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Complete if Known			
Application Number	10/689,390		
Filing Date	20 October 2003		
First Named Inventor	BEAUMONT, Mark		
Art Unit	2183		
Examiner Name	PETRANEK, Jacob Andrew		
Attorney Docket Number	PAT001063-000 13721-7-999		

	U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2 (Finom)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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		Country Code <sup>3</sup> 'Number <sup>4</sup> 'Kind Code <sup>5</sup> (if known)	MM-DD-YYYY		Or Relevent Figures Appear	T <sup>6</sup>
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Signature /Jacob Petranek/ considered 12/07/2009	Examiner Signature	/Jacob Petranek/	Date Considered	12/07/2009
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete in raison,			
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First Named Inventor	BEAUMONT, Mark		
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Examiner Signature	/Jacob Petranek/	Date Considered	12/07/2009

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INFORMATION DISCLOSUR	RE Filing Date	20 October 2003			
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(Use as many sheets as necessary)	Art Unit	2183			
(Use as many sheets as necessary)	Examiner Name	PETRANEK, Jacob Andrew			
Sheet 3 of 3	Attorney Docket Number	PAT001063-000 13721-7-999	7		

	NON PATENT LITERATURE DOCUMENTS	
Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
29	HANOUNIK, et al.; Linear-Time Matrix Transpose Algorithms Using Vector Register File With Diagonal Registers; 2001; pp. 1-8	
30	CHOI, et al.; Parallel Matrix Transpose Algorithms On Distributed Memory Concurrent Computers; 1993; pp. 1-20	
31	HUANG, Han-Way; MC68HC12 An Introduction: Software and Hardware Interfacing; 1 July 2002; Delmar Cengage Learning; 1 Edition; pp. 62, 63, 66.	
	No. <sup>1</sup> 29	Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, jumal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.  HANOUNIK, et al.; Linear-Time Matrix Transpose Algorithms Using Vector Register File With Diagonal Registers; 2001; pp. 1-8  CHOI, et al.; Parallel Matrix Transpose Algorithms On Distributed Memory Concurrent Computers; 1993; pp. 1-20  HUANG, Han-Way; MC68HC12 An Introduction: Software and Hardware

Signature	/Jacob Petranek/	Date Considered	12/07/2009	

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